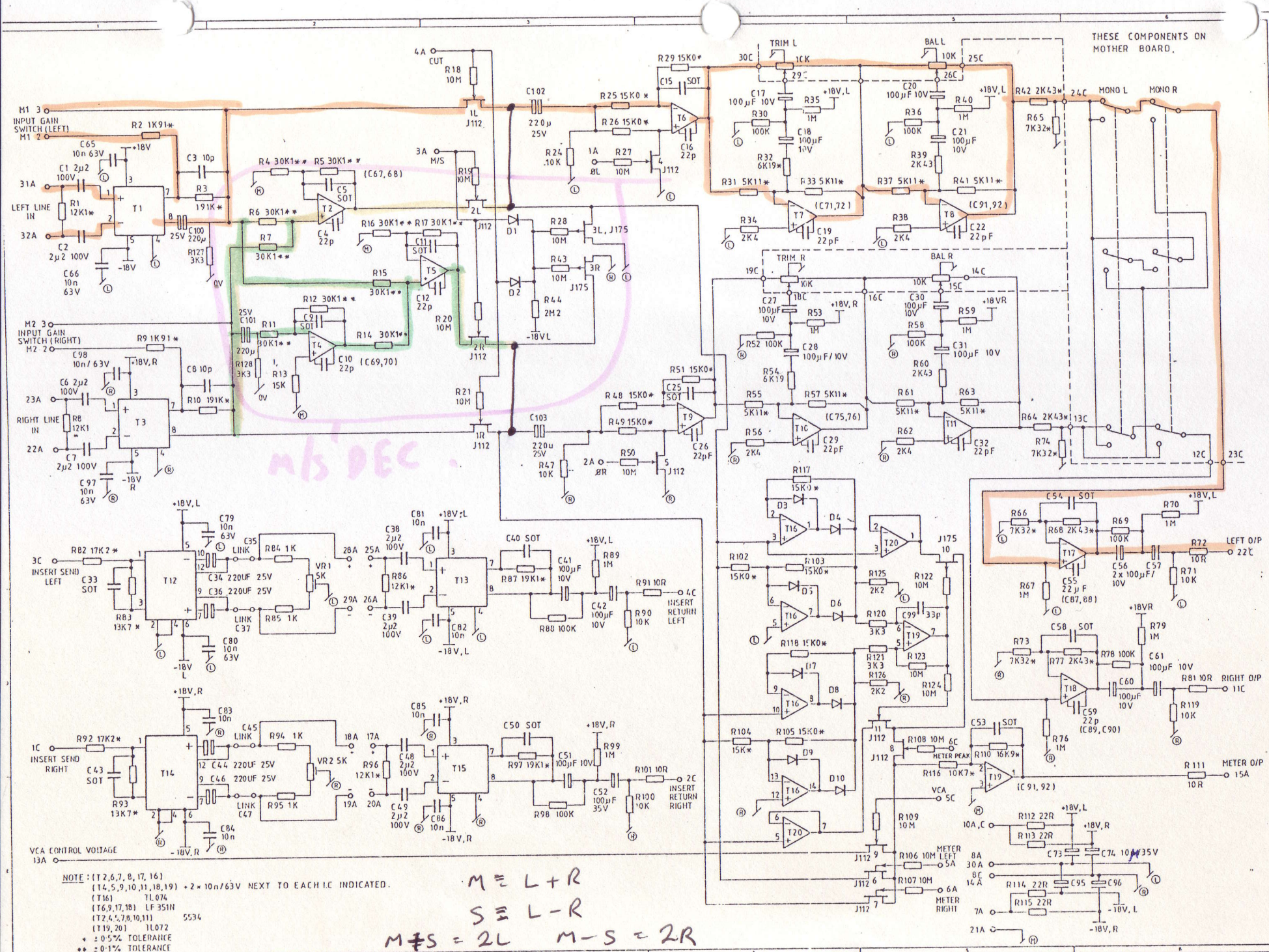

NOTE: (T2,6,7,8,17,16) (T4,5,9,10,11,18,19) +2 × 10n/63V NEXT TO EACH I.C INDICATED.
(T16) TL074
(T6,9,17,18) LF 351N
(T2,4,5,7,8,10,11) 5534
(T19,20) TL072
* ±0·5% TOLERANCE
** ±0·1% TOLERANCE

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Rev.	Issue	Chk'd	Details
P	a	7·84	PROTOTYPE
1	a	7·84	R65 74 MOVED & CHANGED VALUE R67 R76 TO 1M
2	a	7·84	R117, 118 ADDED
2	b	N/A 3·85	METER PEAK CIRCUITRY CHANGED
2	c	1·85	MS AND 0 VALUES CHANGED
2	d	7·84	REDRAWN D.D.
3	a	4·85	R120, 121 CHANGED 2×2K2's ADDED
3	b	4·85	R44 NOW 2M2
4	a	7·85	R84, 85, 94, 95 VALUE NOW 1K. VR1, VR2 VALUE NOW 5K
4	b	BC Apr86	ECON ADDRESSES ADDED NOTE AMENDED SL
4	c	DTC June86	MONO L & MONO R REVERSED SL
5	a	Dec86	R22, 23, 45 & 46: C13, 14 23 & 24 DELETED:- R128 3K3 + R127 3K3 & C100, 101, 102 + 103 220µ ADDED EL
6	a	May87	T17 & 18 IC LIN AMP NOW IC LIN BIFET AMP REF: LF351N
7	a	DM May87	R82, 83, 92 & 93 WERE 5K11. C34-7 & C44-7 WERE CAP ELECT. 220UF 25V. EL

Title 82E201 STEREO LINE INPUTS INSERTS SENDS & RETURNS.

Drg. No. T82201·70

Solid State Logic

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