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REV	ISSUE	DATE	DETAILS
A	25 JAN 88	NEW DRAWING DTG	STEFF
B	28 FEB 89	B2E01 DELETED B2E291 INSERTED	STEFF
C	11 SEP 92	DCR/B70 REF CORRECTED FROM IC10 TO IC12 BETWEEN B2E291 PINS S1 AND S3 MM	

SHEET 1 OF 1	
REF:	T611GB A2

TITLE	611G SERIES I/O MODULE BLOCK SCHEMATIC
DRG. NO.	T0611G.41
Solid State Logic	